

Docket No.: P2002,0599

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By:  Date: August 4, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Till Schlösser et al.
Appl. No. : 10/619,970
Filed : July 15, 2003
Title : Integrated Semiconductor Memory and Fabrication Method
Art Unit :

LETTER

Hon. Commissioner for Patents,
Alexandria, VA 22313-1450

Sir:

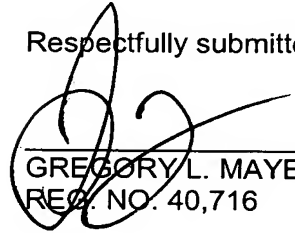
The above-mentioned new patent application was filed on July 15, 2003 without a signed oath or declaration, under the provision of 37 C.F.R. 1.53(f).

In accordance with the above-mentioned rule, enclosed herewith is the original signed declaration.

The undersigned hereby states that the application filed in the Patent and Trademark Office is the application which the inventor(s) executed by signing the declaration. MPEP 602 (8th ed., Aug. 2001).

The fee required for the late filing of an oath or declaration in the amount of \$130.00 is also enclosed.

Respectfully submitted,



GREGORY L. MAYBACK
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/mjb

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